



# Bhoj Reddy Engineering College for Women

(Sponsored by Sangam Laxmibai Vidyapeet, approved by AICTE and affiliated to JNTUH)  
Vinaynagar, IS Sadan Crossroads, Saidabad, Hyderabad – 500 059, Telangana. www.brecw.ac.in

## Department of Electrical and Electronics Engineering Time Table for IV B Tech I Semester, Academic Year 2023-24

Room No : NB-G09

w e f : 31 July 2023

Time (Hrs)	9:30-10:30	10:30-11:30	11:30 - 12:30	12:30 - 13:30	13:30-14:30	14:30-15:30	15:30-16:30	
Day	1	2	3	L U N C H	4	5	6	
Monday	HVDCT	<----- DSP----->			<----- JP ----->		FME	
Tuesday	<----- JP ----->		FME		DSP	<----- HVDCT ----->		
Wednesday	<----- EED LAB----->				FME	<----- DSP ----->		
Thursday	<----- SEMINAR ----->				<----- HVDCT ----->		JP	
Friday	<----- EED LAB ----->				<----- IOMP/SI ----->			
Saturday	<----- PS-I ----->				<----- PS-I ----->			

S No	Course Code	Course Title	Room No	Faculty	Department
1	CS703OE	Java Programming (JP)	NB-G09	S Revathi	IT
2	EE712PE	Digital Signal Processing (DSP)	NB-G09	Staff X	ECE
3	EE721PE	HVDC Transmission (HVDCT)	NB-G09	K Umarani	EEE
4	SM701MS	Fundamentals of Management for Engineers (FME)	NB-G09	Y Sagarika	BA
5	EE701PC	Electrical & Electronics Design Lab (EED Lab)	NB-G11	B Dhanadeepika & K Umarani	EEE
6	EE702PC	Industrial Oriented Mini Project (IOMP)/Summer Internship (SI)	NB-G10	S Asha Kiranmai	EEE
7	EE703PC	Seminar	WB-401	S Deepti, R Manju Bhargavi & S Mayuri	EEE
8	EE704PC	Project Stage-I (PS-I)	NB-G10	S Swathi	EEE

S Asha Kiranmai  
Incharge

S Deepti  
HOD-EEE

G Dayakar Reddy  
Vice-Principal

Dr E Madhusudhana Reddy  
Principal